

Notice of References Cited	Application/Control No. 10/589,109		Applicant(s)/Patent Under Reexamination STORMS ET AL.	
	Examiner BRANDON S. COLE		Art Unit 2816	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-7,030,654	04-2006	Mentze et al.	326/83
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Wikipedia - Clock Signal http://en.wikipedia.org/wiki/Clock_signal			
	V	Wikipedia - Inverter (logic gate) http://en.wikipedia.org/wiki/Inverter_%28logic_gate%29			
	W	Wikipedia - Integrated circuit http://en.wikipedia.org/wiki/Integrated_circuit			
	X				

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